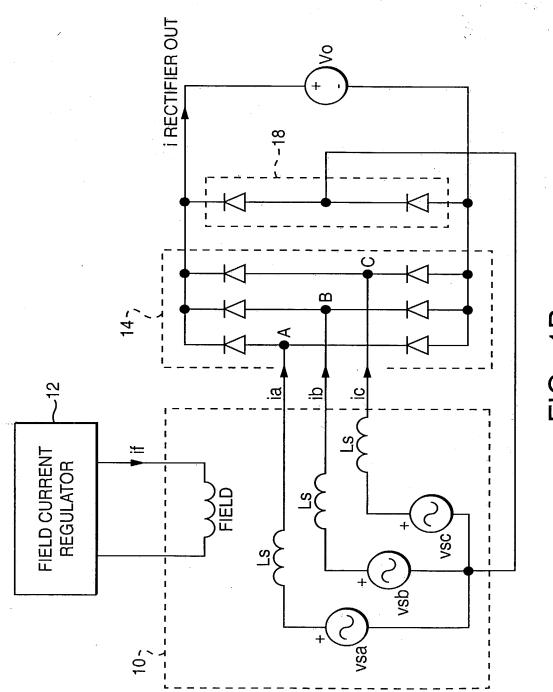
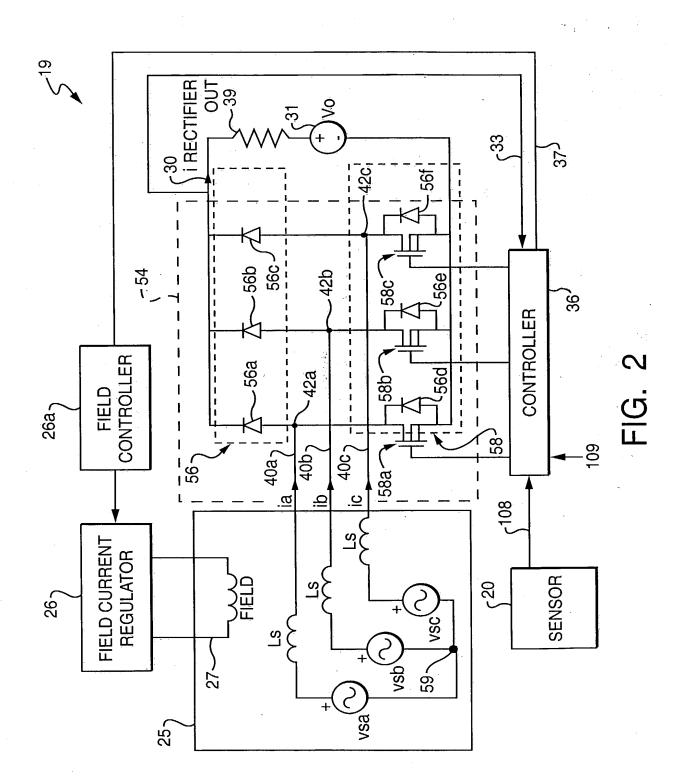
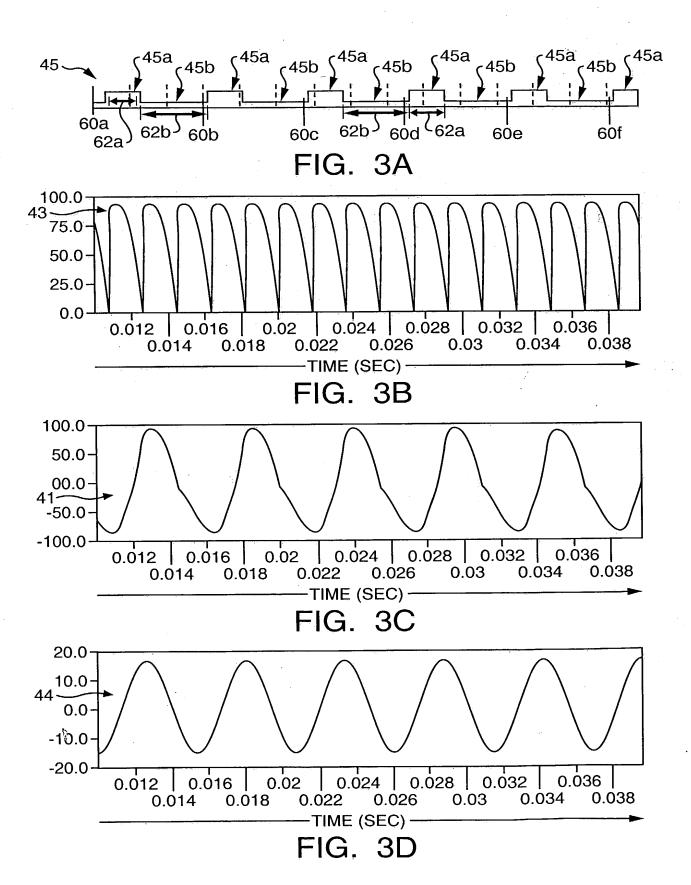


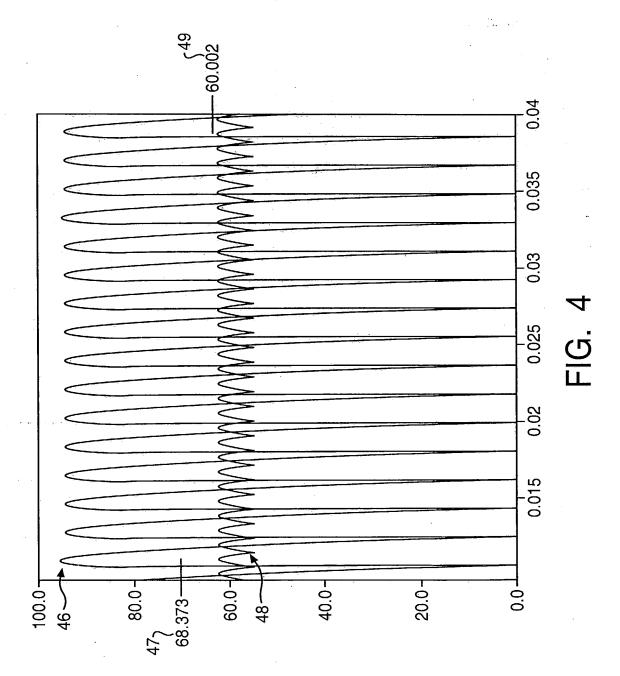
FIG. 1A (PRIOR ART)

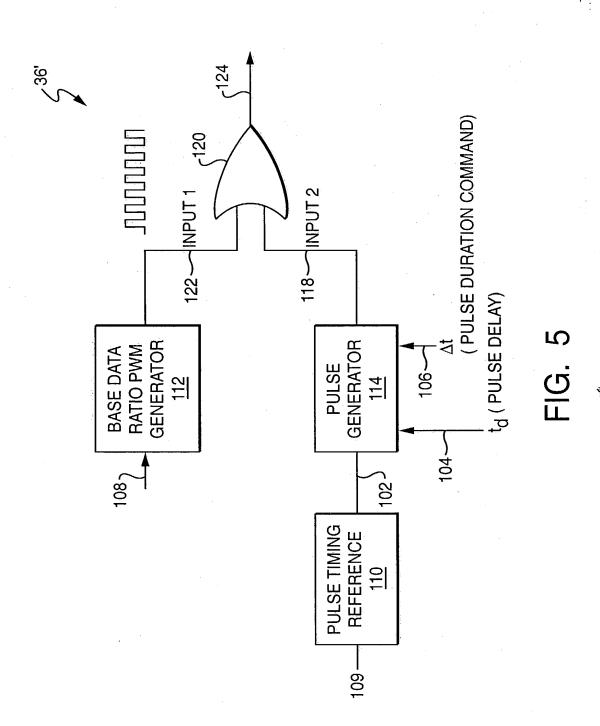


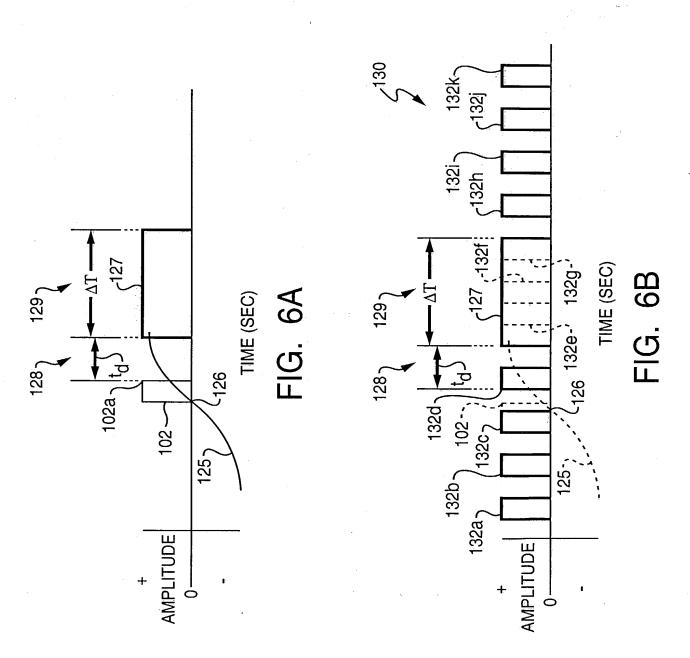


Application No.: 10/080,819

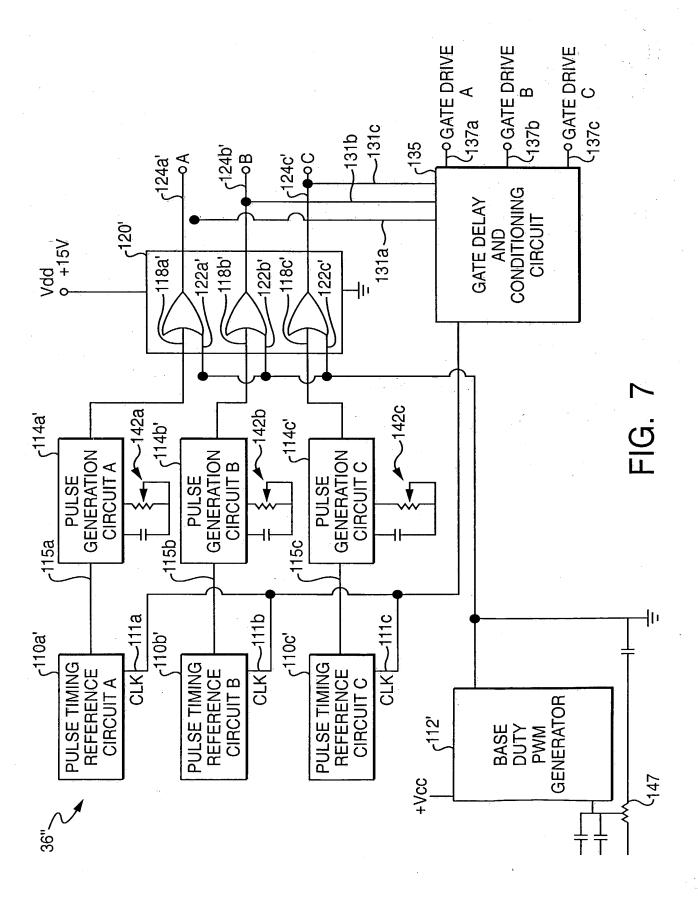




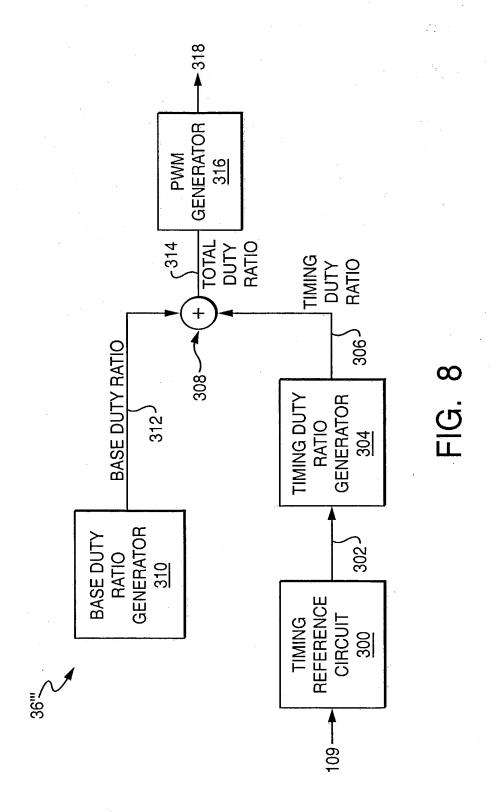


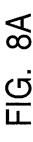


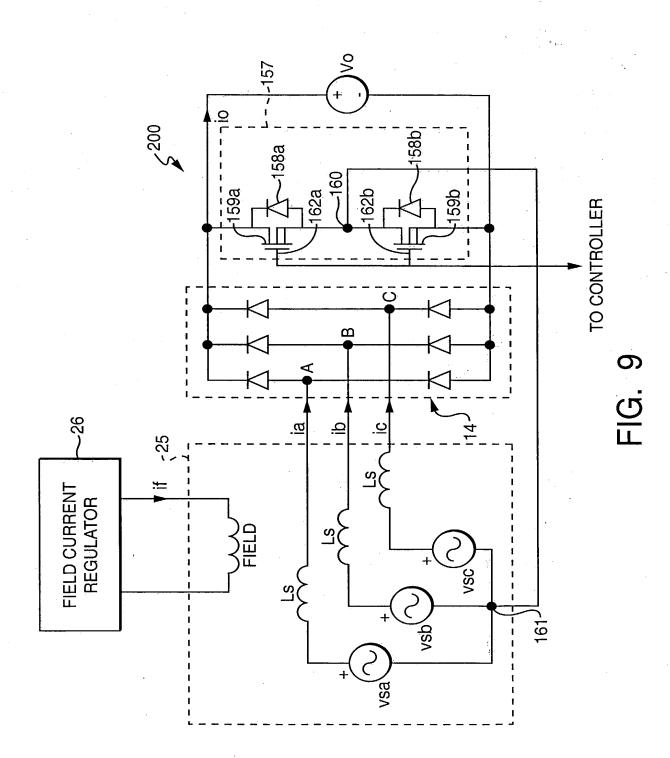
Thomas A. Keim, et al. Application No.: 10/080,819

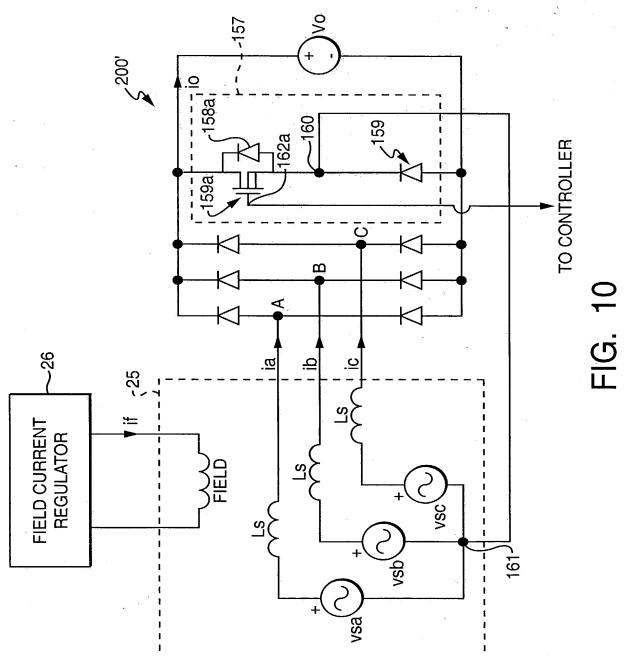


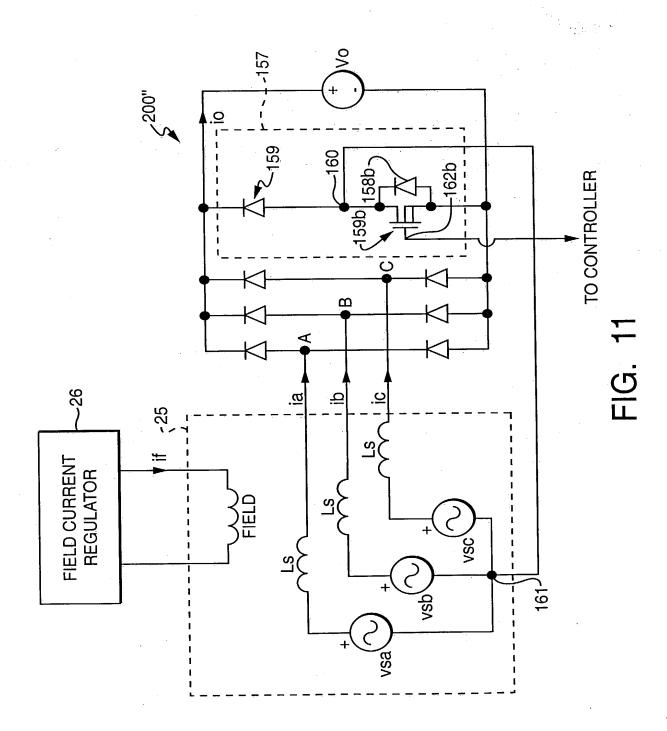
OR CONTROL CIRCUIT AND RELATED
Thomas A. Keim, et al.
Application No.: 10/080,819

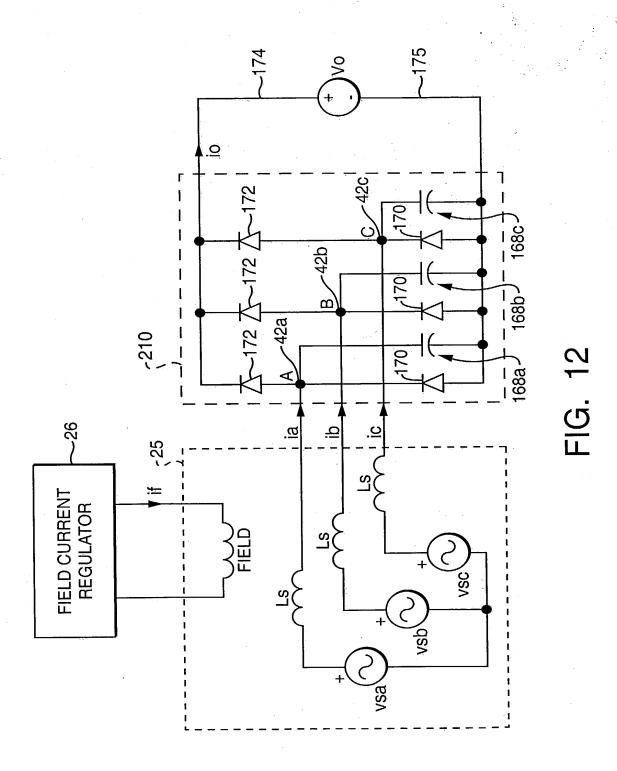


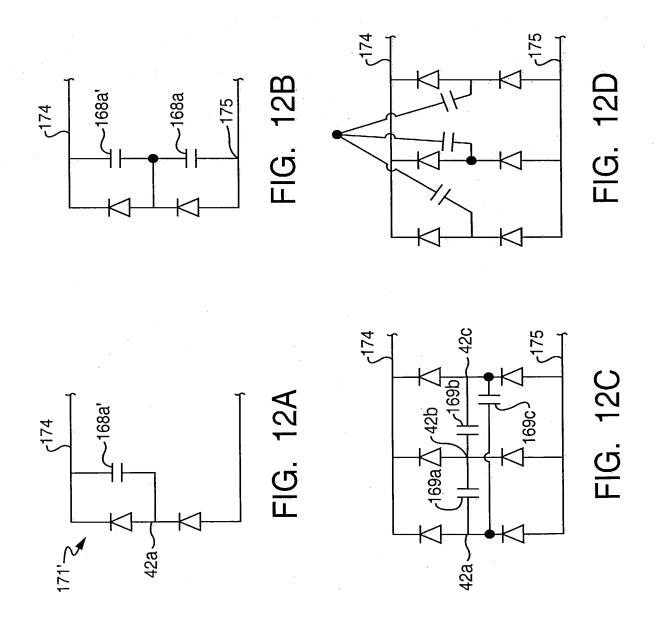






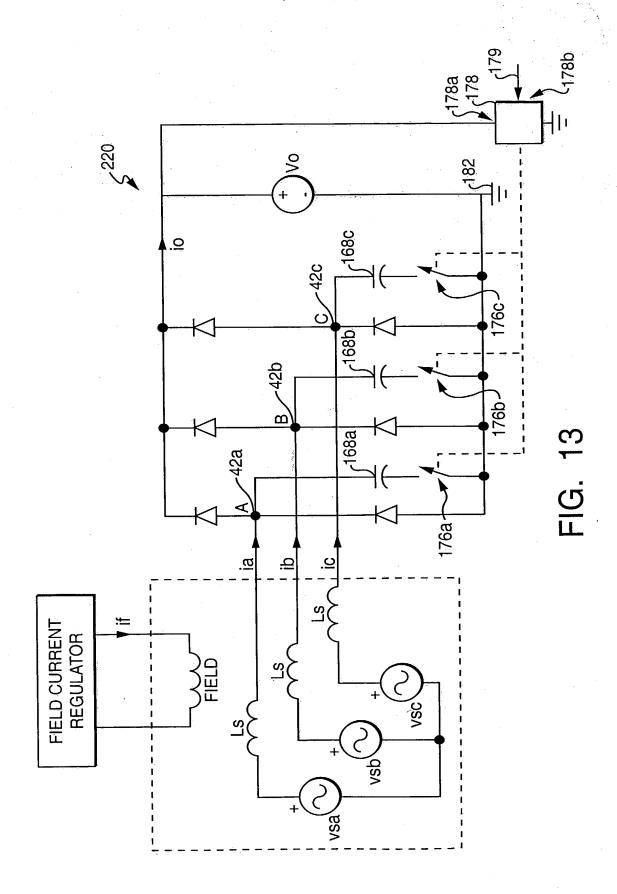


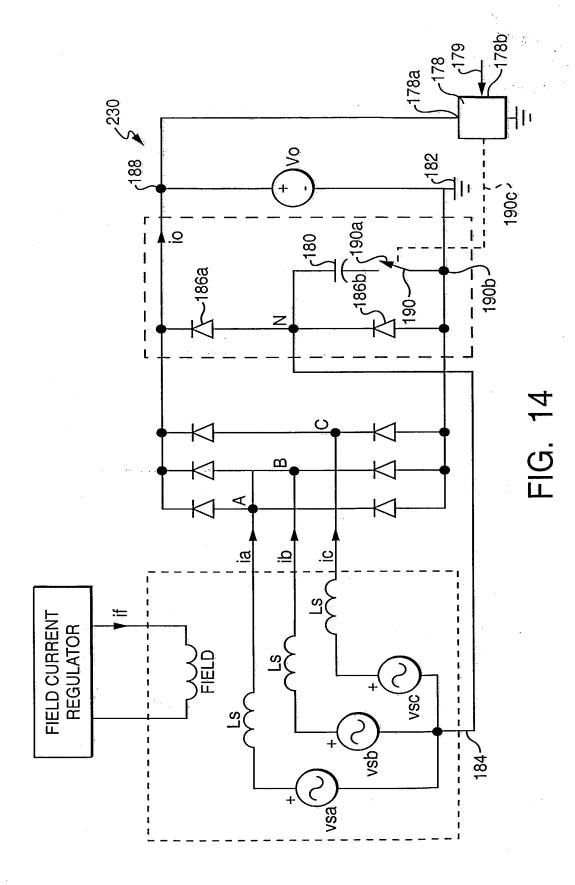




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